Electronic Acknowledgement Receipt				
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Title of Invention:	MULTI-LAYER INTEGRATED SEMICONDUCTOR STRUCTURE HAVING AN ELECTRICAL SHIELDING PORTION			
First Named Inventor/Applicant Name:	Rafael Reif			
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)			
1	TO_PTO_Cert_Corr.pdf	72147	yes	2				
		61e7bfe8400fe363c4c359993fd9ddbbc 1e5dd15						
Multipart Description/PDF files in .zip description								
	Document Description		Start	End				
	Miscellaneous Incoming Letter		1	1				
	Request for Certificate of Correction		2	2				
Warnings:								
Information:								
2 Fee Worksheet (PTO-06)	fee-info.pdf	8202	no	2				
		43afd61da6527add41706d02496ff6c4a 53ec78d						
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